

Enhanced Split-CV method for mobility extraction minimizing V_{DS}-effects

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Abstract: Carrier mobility is an important figure of merit for understanding transistor device physics. Unlike bias and current, mobility is extracted indirectly from measurements such as I-V and C-V characteristics. Consequently, the choice of extraction technique is important. While the split-C-V technique remains the gold standard for determining carrier mobility, studies by Liu et al. demonstrated that V_{DS} significantly impacts the extracted mobility in advanced devices with an EOT of approximately 1 nm. To address this, an improved methodology was proposed based on the arithmetic average of split-C-V results for positive and negative V_{DS} values. This paper proposes an alternative approach based on the geometric mean of these results, further reducing the impact of V_{DS} on the extracted mobility.

RESULTS AND DISCUSSION

The traditional split-C-V method relies on two primary measurements: the I_D-V_G characteristic at a low drain bias and the gate-to-channel capacitance (C_{gc}-V_G)

$$Q_{inv}(V_G) = \int_{-\infty}^{V_G} C_{gc}(V) dV \quad (1)$$

$$\mu_{eff} = \frac{L}{W} \cdot \frac{I_D(V_G)}{V_{DS} \cdot Q_{inv}(V_G)} \quad (2)$$

Based on this standard approach, several studies have proposed methodologies to further refine extraction accuracy [1], [2], [3], [4]

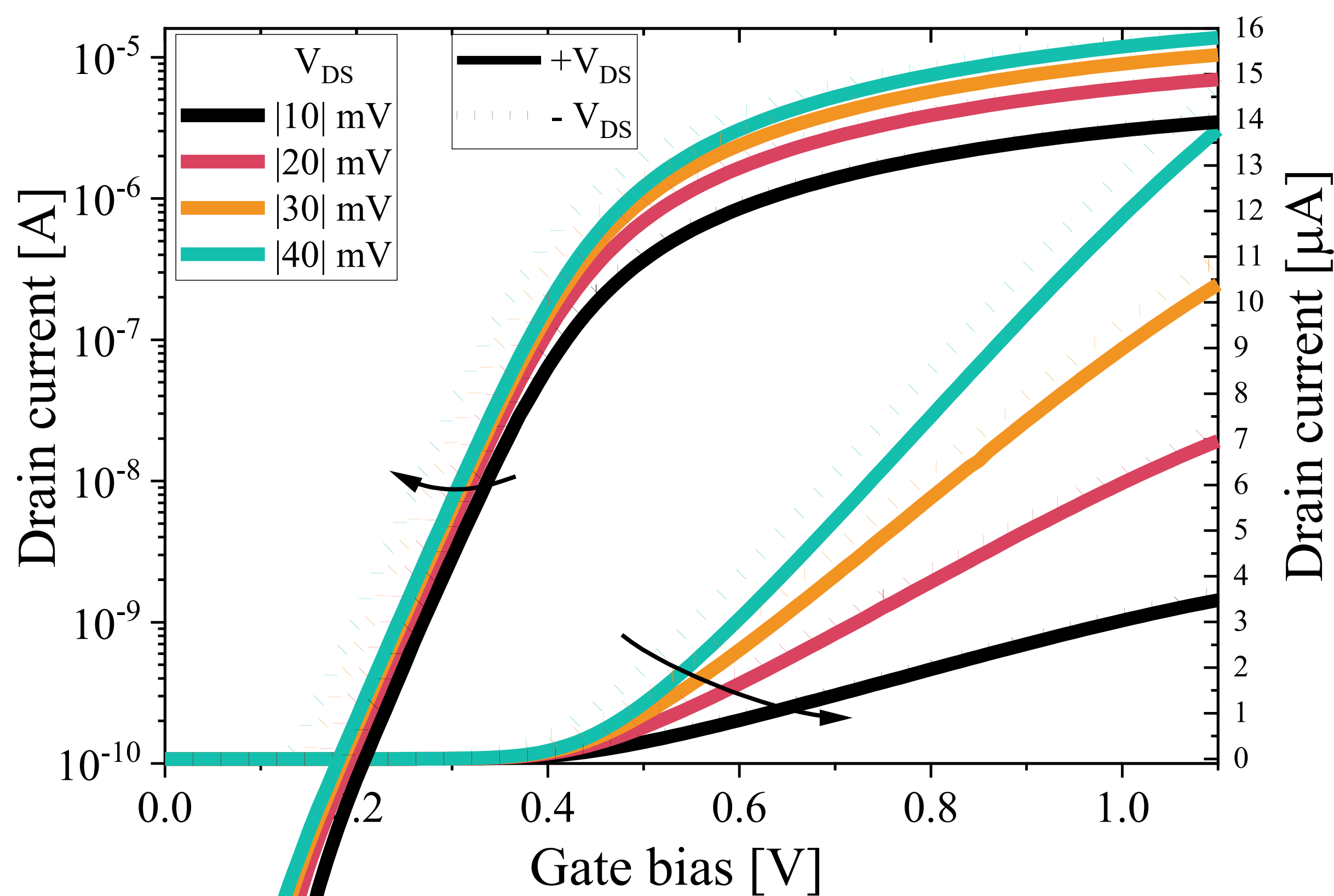


Fig. 1. Drain current as a function of the gate bias for a wide range of drain bias, from -40 mV to +40 mV.

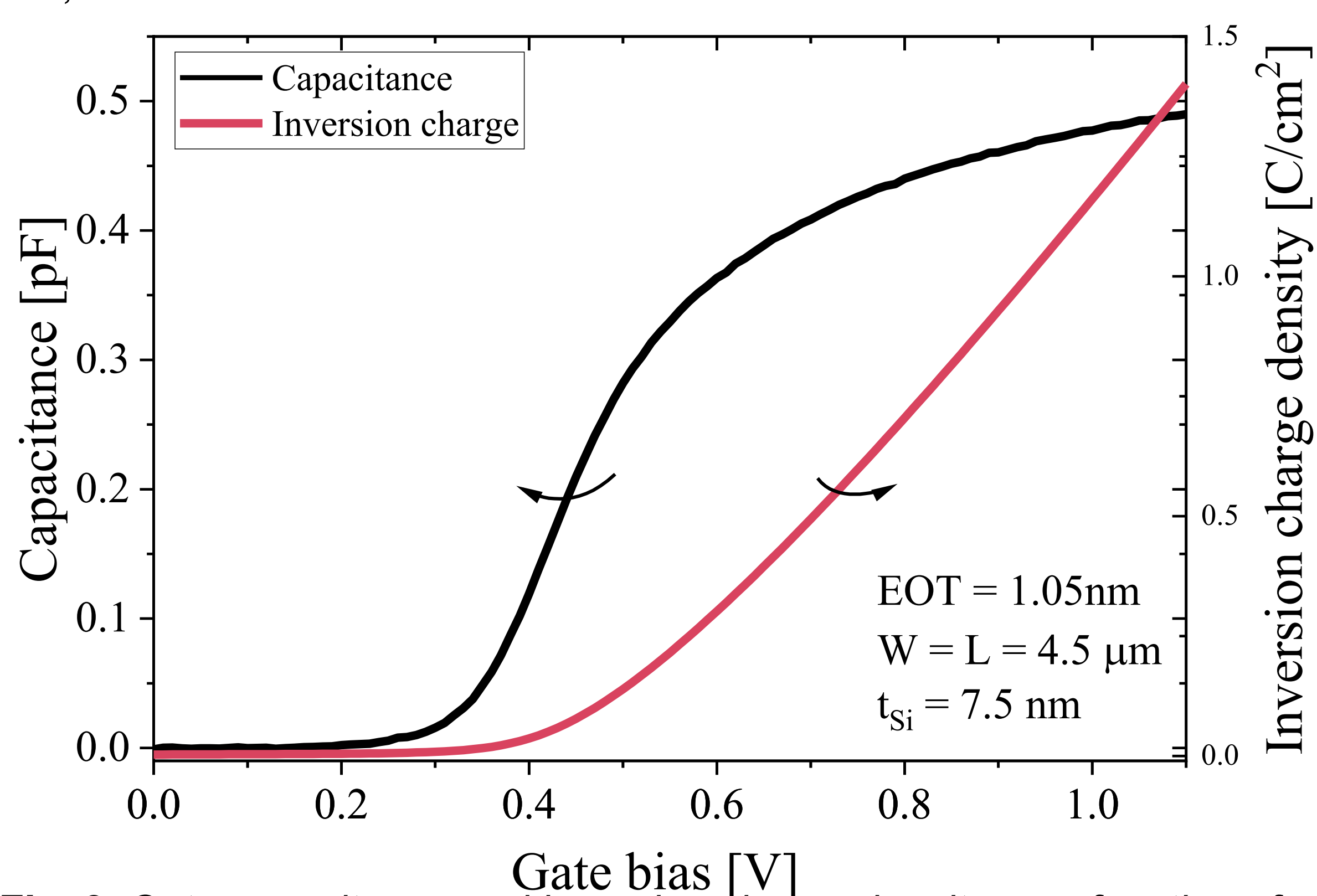


Fig. 2. Gate capacitance and inversion charge density as a function of gate bias.

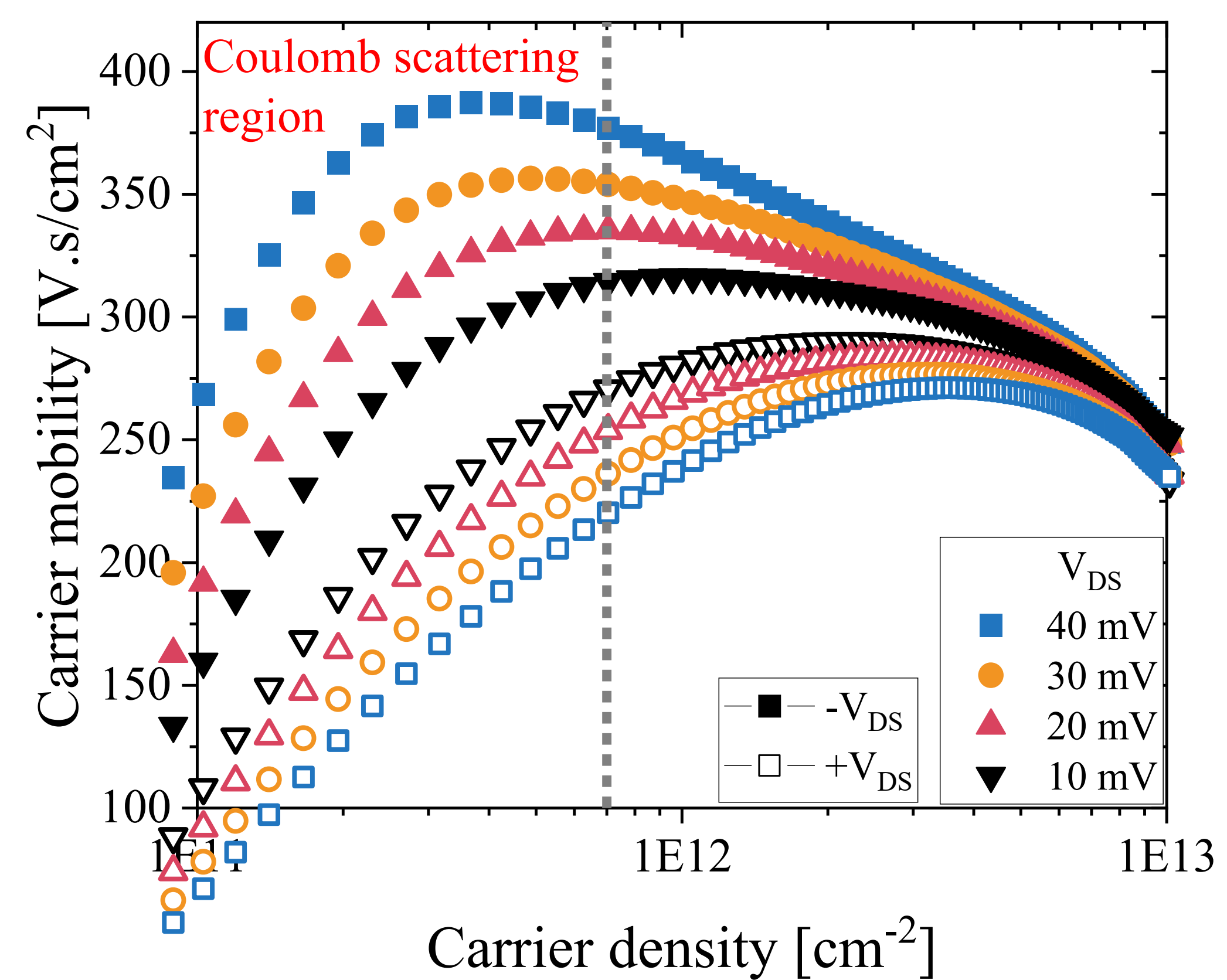


Fig. 3. Carrier mobility as a function of carrier density obtained with drain bias from -40 mV to +40 mV

The comparison suggests that the geometric mean is more effective at neutralizing the V_{DS} impact across the N_{inv} range. This improvement is attributed to the power-law behavior of the mobility curve in the low-density region

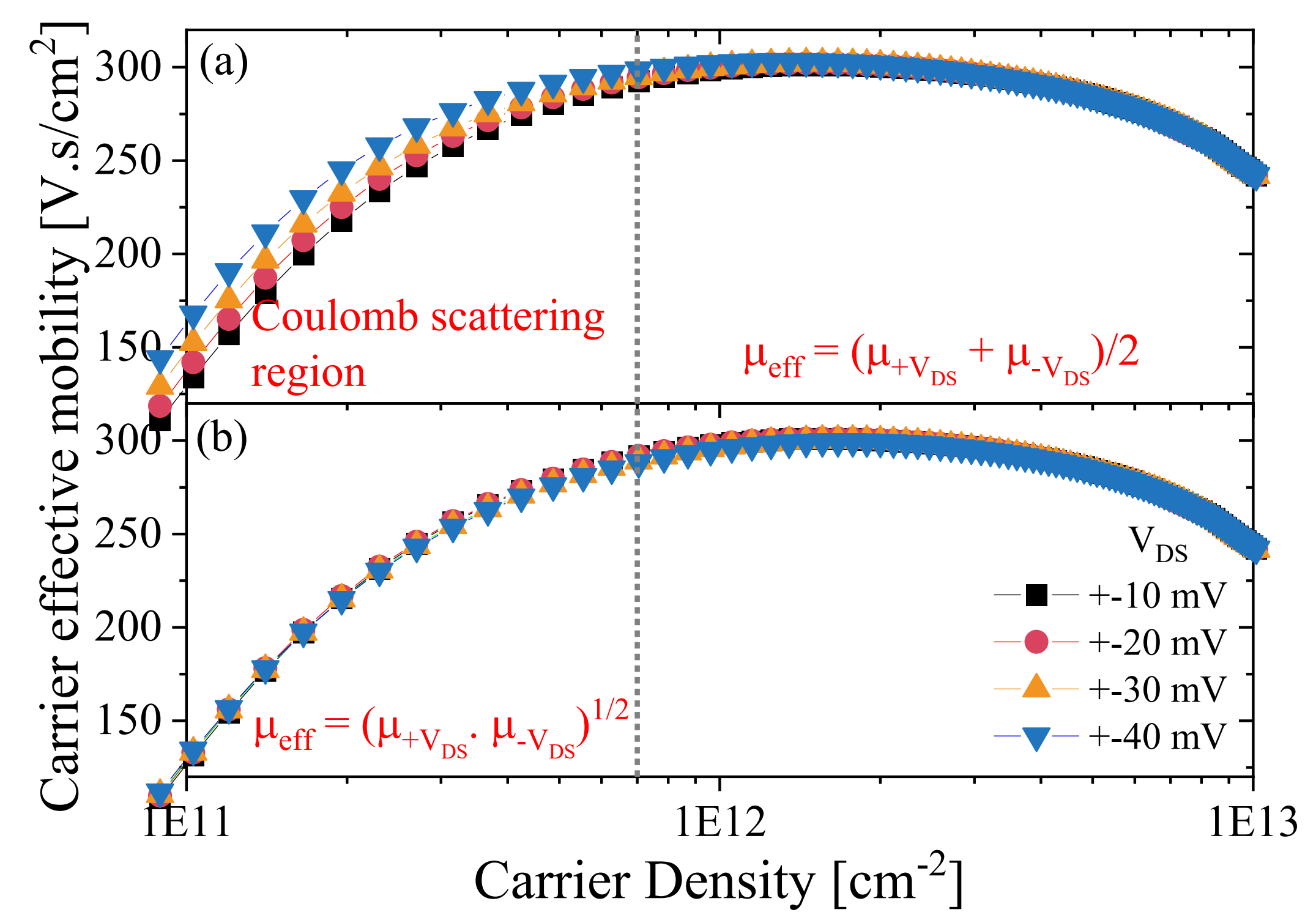


Fig. 4. Carrier mobility as a function of carrier density calculated (a) using a simple average and (b) using a geometric mean

REFERENCES

- [1] Z. Liu et al., "Intrinsic effective mobility extraction with extremely scaled gate dielectrics," *Appl. Phys. Lett.*, vol. 97, no. 2, p. 023509, 2010.
- [2] K. Uchida and S. Takagi, "Carrier scattering induced by thickness fluctuation of SOI film," *Appl. Phys. Lett.*, vol. 82, no. 17, pp. 2916–2918, 2003.
- [3] L. Pirro et al., "Split-CV for pseudo-MOSFET characterization," in *Proc. IEEE Int. Conf. Microelectron. Test Struct. (ICMTS)*, 2014, pp. 14–19.
- [4] G. Ghibaudo, "Mobility Characterization in Advanced FD-SOI CMOS Devices," in *Semiconductor-On-Insulator Materials for Nanoelectronics Applications*, Springer, 2011, pp. 307–322.
- [5] F. Gamiz et al., "Coulomb scattering model for ultrathin silicon-on-insulator inversion layers," *Appl. Phys. Lett.*, vol. 80, no. 20, pp. 3835–3837, 2002.
- [6] F. Jiménez-Molinos et al., "Coulomb scattering in high-k gate stack SOI MOSFETs," *J. Appl. Phys.*, vol. 104, no. 6, p. 063704, 2008.

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